



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: James S. Zeng et al.
Title: Digital Loop for Regulating DC/DC Converter with
Segmented Switching
Application No.: 10/726,723
Filing Date: December 3, 2003
Examiner: Shawn Riley
Group Art Unit: 2838
Confirmation No.: 6366
Law Office: Sidley Austin Brown & Wood LLP
Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Examiner Riley:

Applicants submit six (6) sheets of formal drawings, consisting of Figures 1A, 1B, 2, 3, 4, 5, 6, 7, 8A, and 8B, in the above-named application. If there are any questions regarding these drawings, please call the undersigned at (415) 772-7428.

EXPRESS MAIL LABEL NO.:

Respectfully submitted,

EV 611 224 915 US

By:

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November 14, 2005

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